

**REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2185**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:	10/653,754	§	Examiner:	Dillon, Samuel A.
Filed:	September 3, 2003	§	Group/Art Unit:	2185
Inventor(s):		§	Atty. Dkt. No:	5500-97500
	Stephan G. Meier, S. Craig	§		
	Nelson, and Gene W. Shen	§		
		§		
		§		
Title:	Low Power Way-Predicted	§		
	Cache	§		
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		§		
		§		
		§		
		§		

**RESPONSE TO OFFICE ACTION OF
May 28, 2009**

Mailstop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of May 28, 2009, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.